IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chang, et al.

Docket No.:

TSM03-0924

Serial No.:

10/826,956

Art Unit:

2811

Filed:

April 15, 2004

Examiner:

Crane, Sara W.

For:

CMOS Device and Method of Manufacture

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

In response to the Reasons for Allowance, Applicants respectfully respond that the appropriate meaning of the term surface tension, as used in Applicants' specification, is readily apparent from the context in which the term is employed in the specification. By way of example and not limitation, reference is made to, e.g., paragraphs [0010], [0028], [0035] of the specification which place the term surface tension in context. Applicants do not believe the term should be limited or construed as being limited in the manner in which the term is used in the references cited in the Reasons for Allowance.

Respectfully submitted,

January 30, 2007

Date

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